

FIG. 1

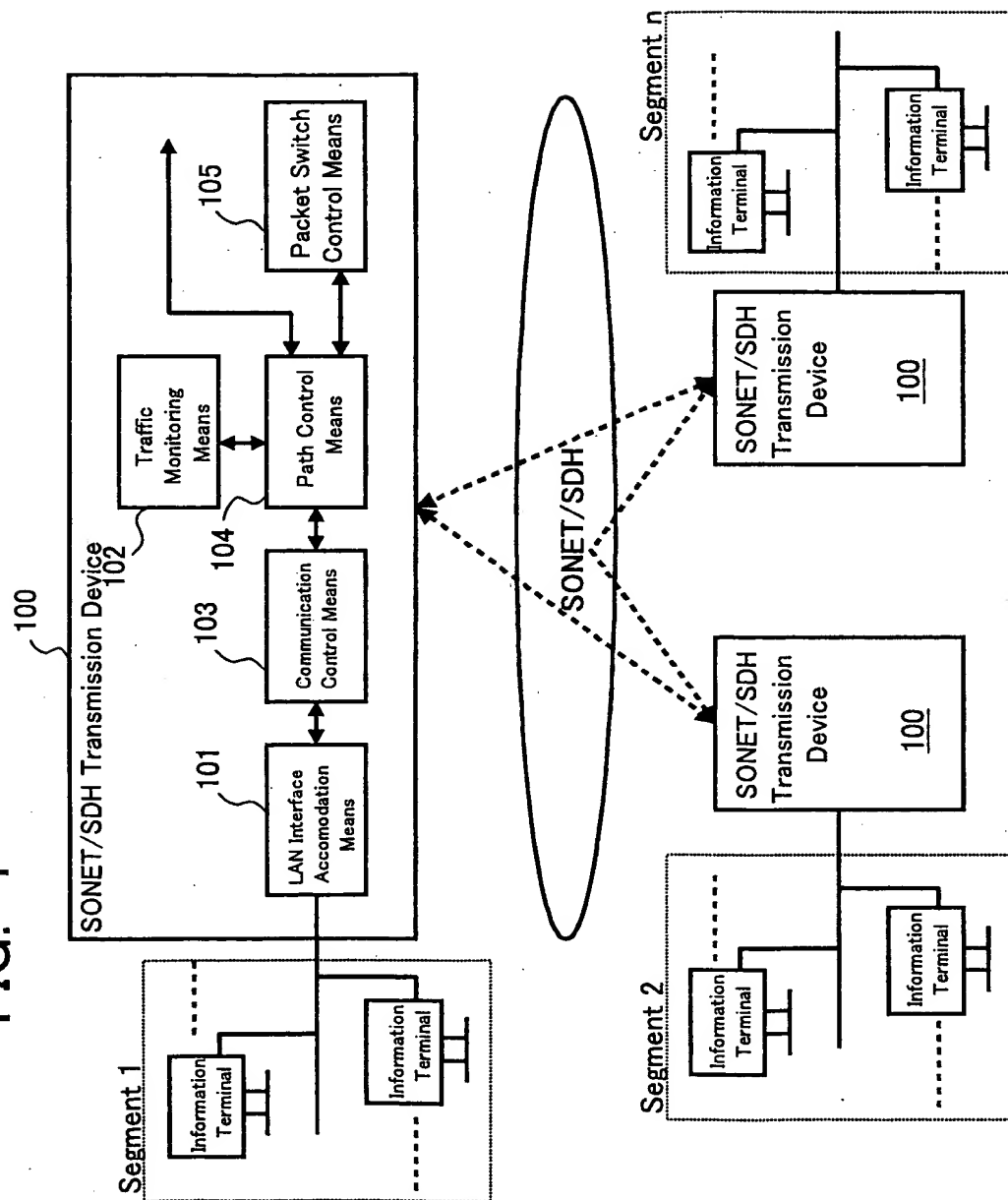
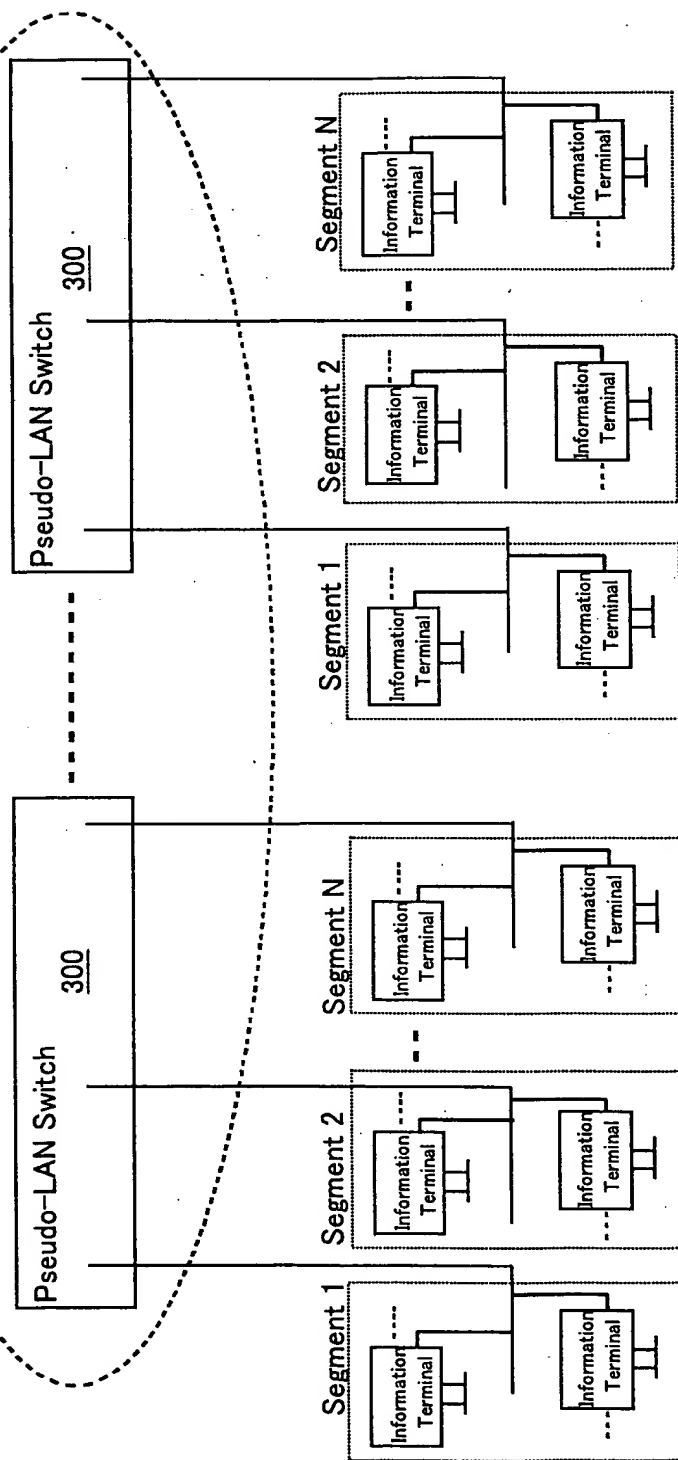
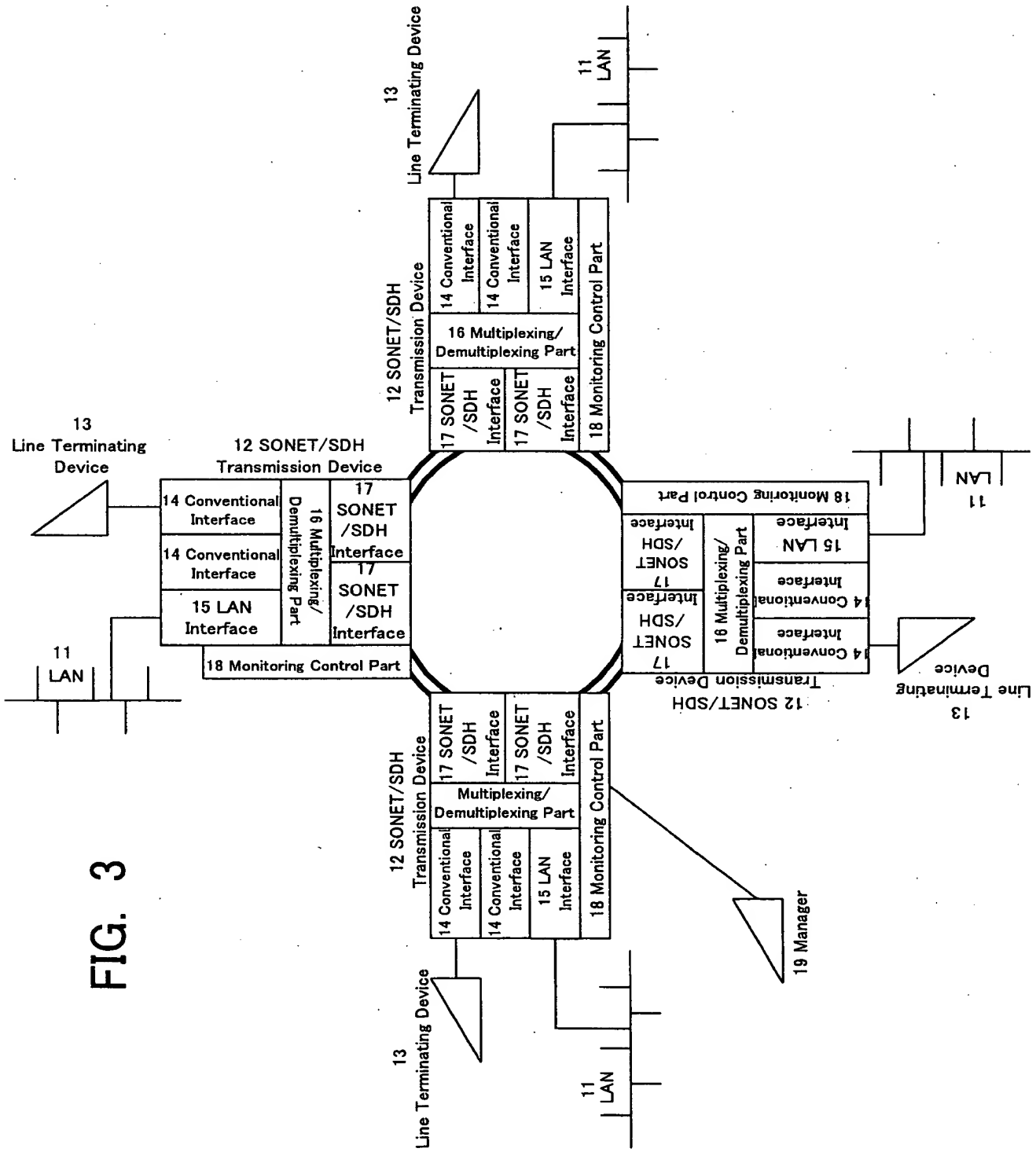


FIG. 2

SONET/SDH System





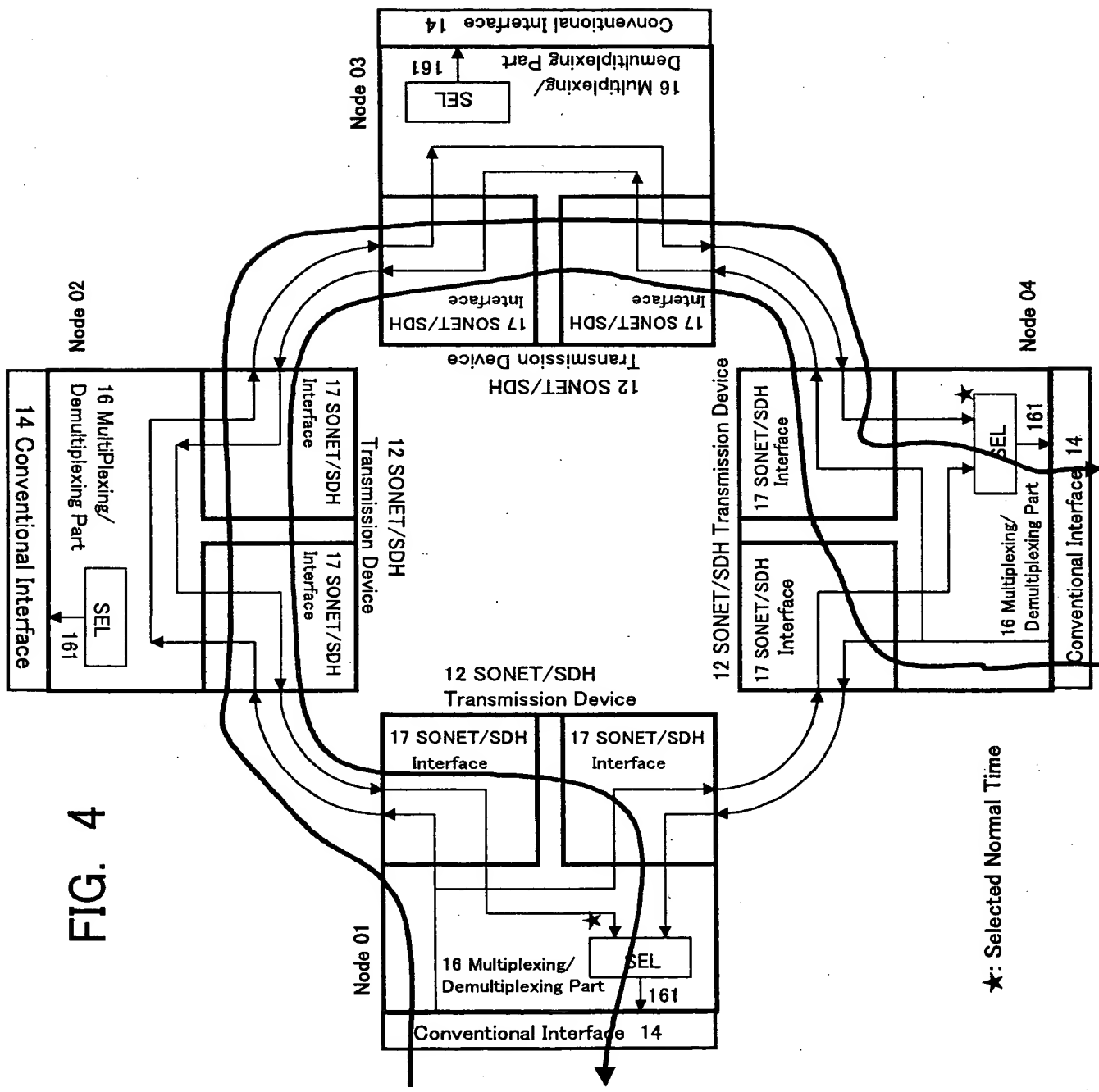


FIG. 4

★: Selected Normal Time

FIG. 5

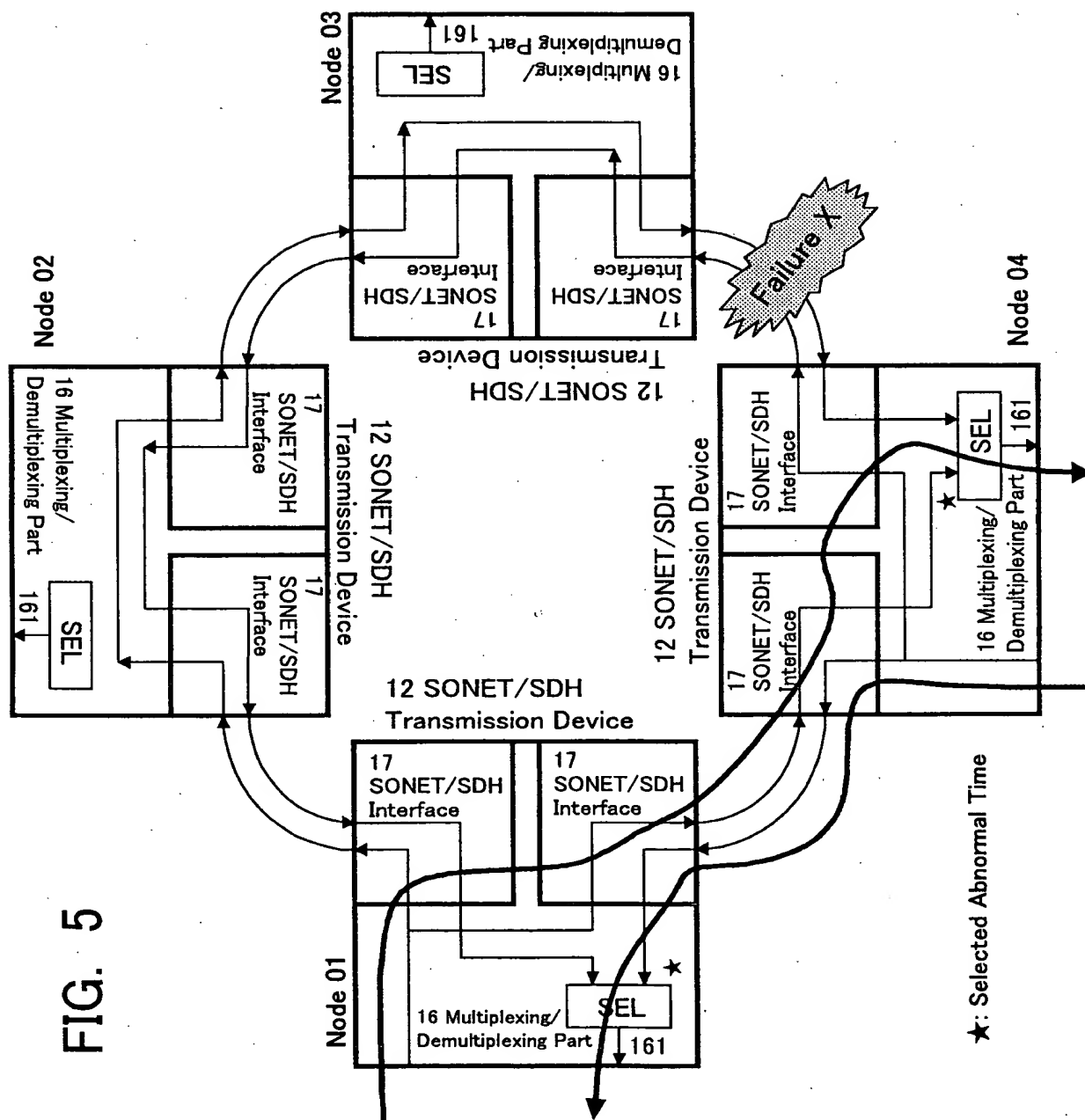


FIG. 6 is a block diagram of a network architecture showing four nodes (Node 01, Node 02, Node 03, and Node 04) interconnected via SONET/SDH interfaces. Each node contains a switch (SW) and a multiplexing/demultiplexing part. The diagram illustrates the flow of data between the nodes and the interfaces used for connection.

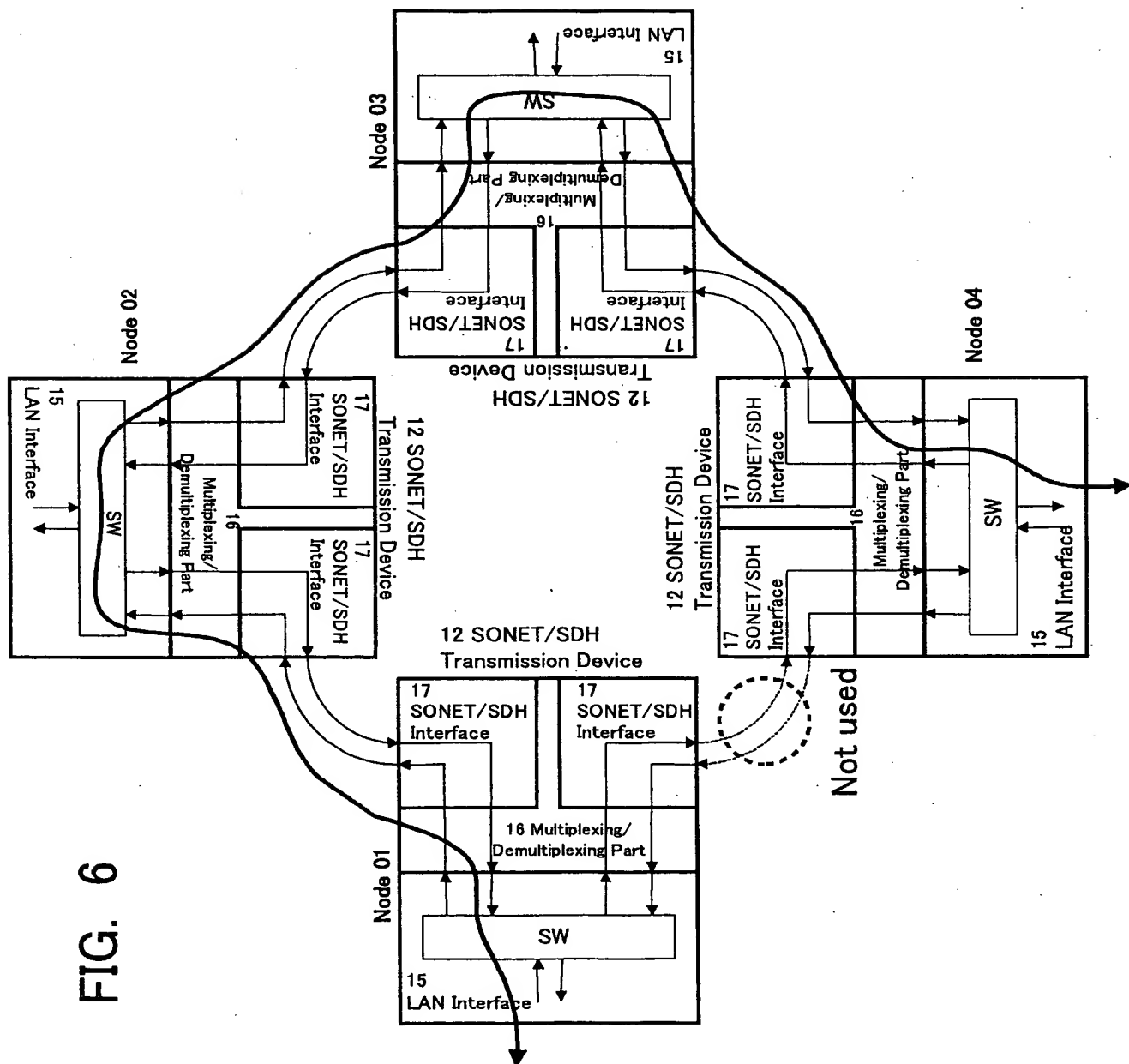


FIG. 6

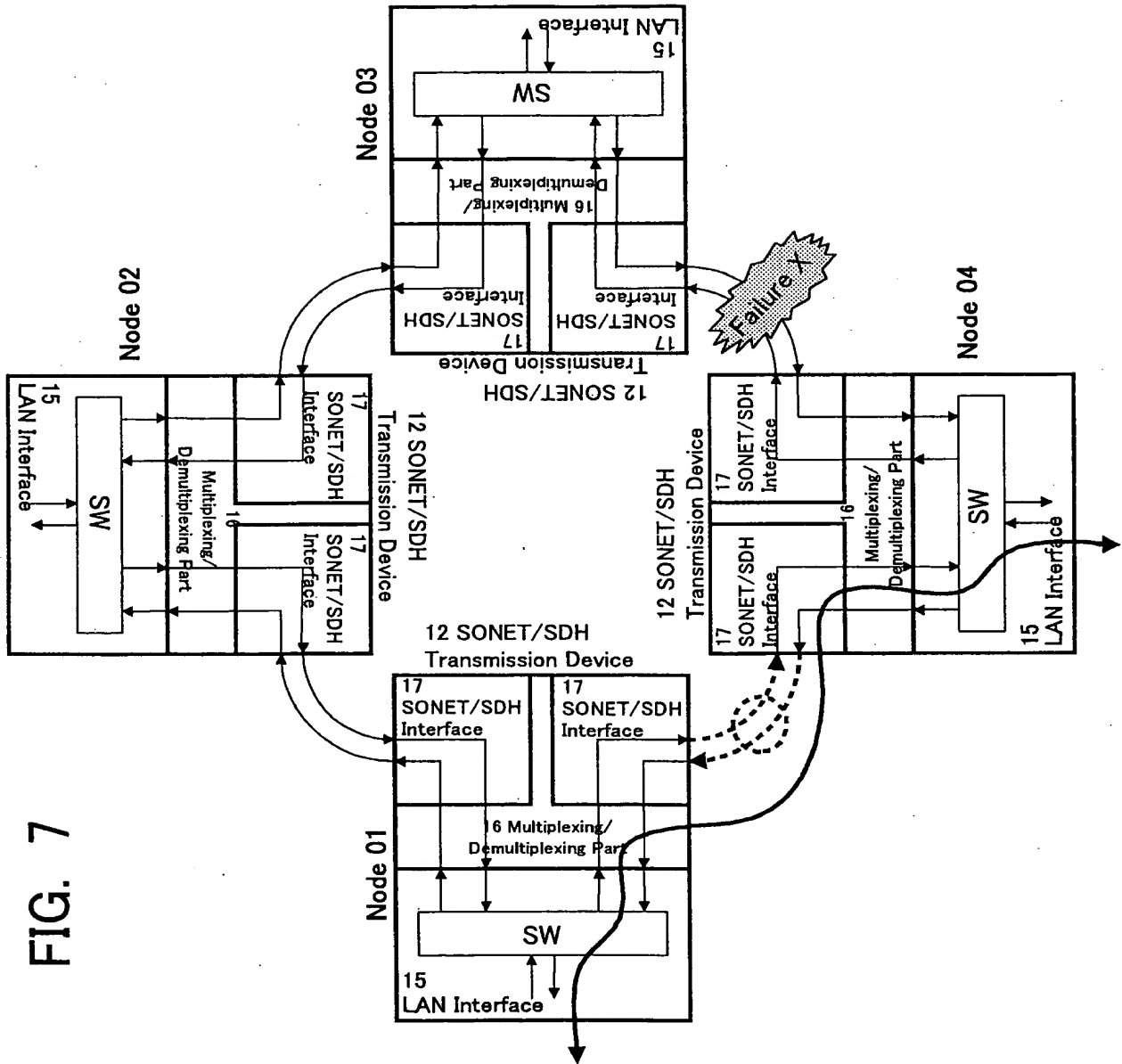


FIG. 7

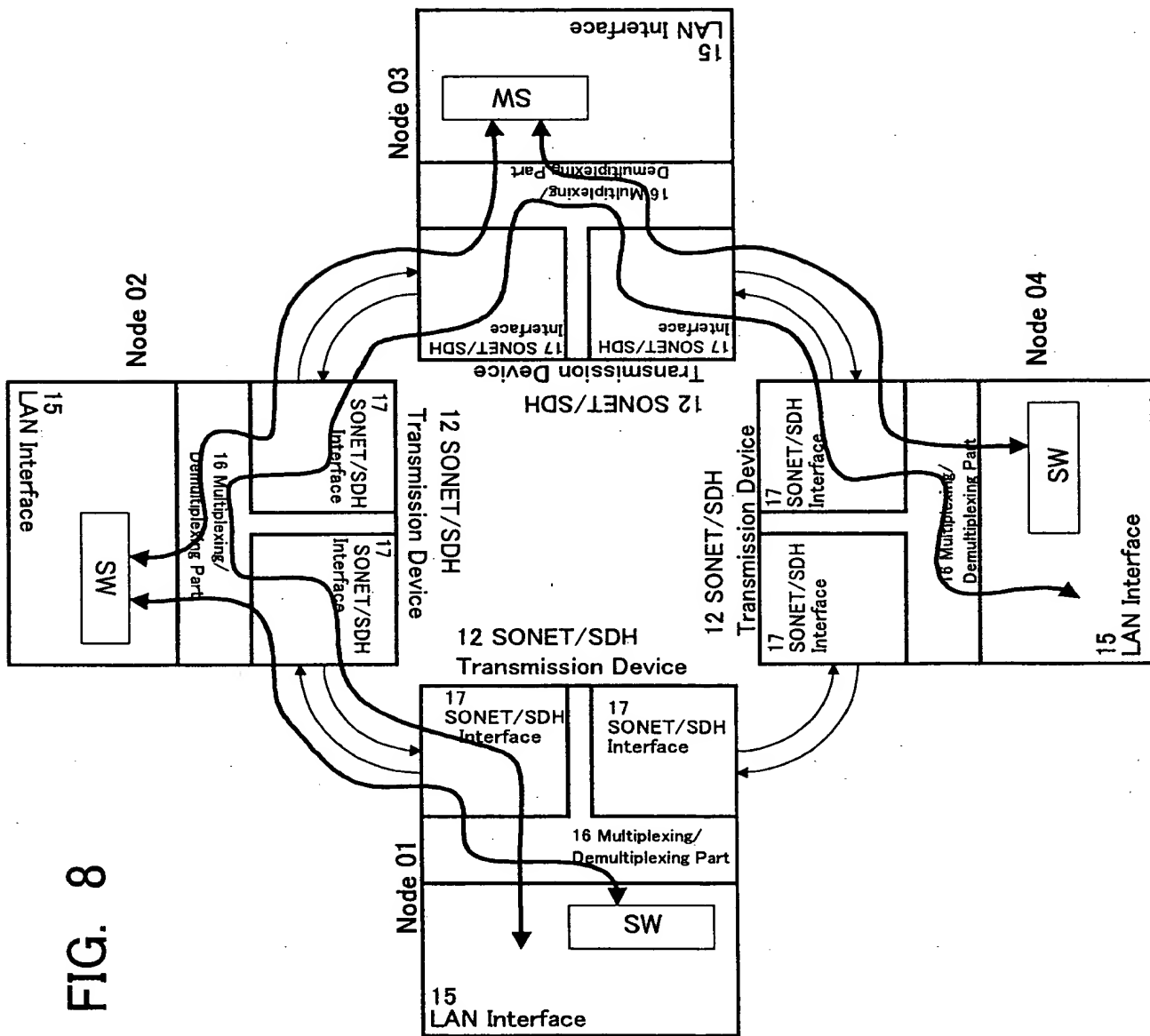


FIG. 8

FIG. 9

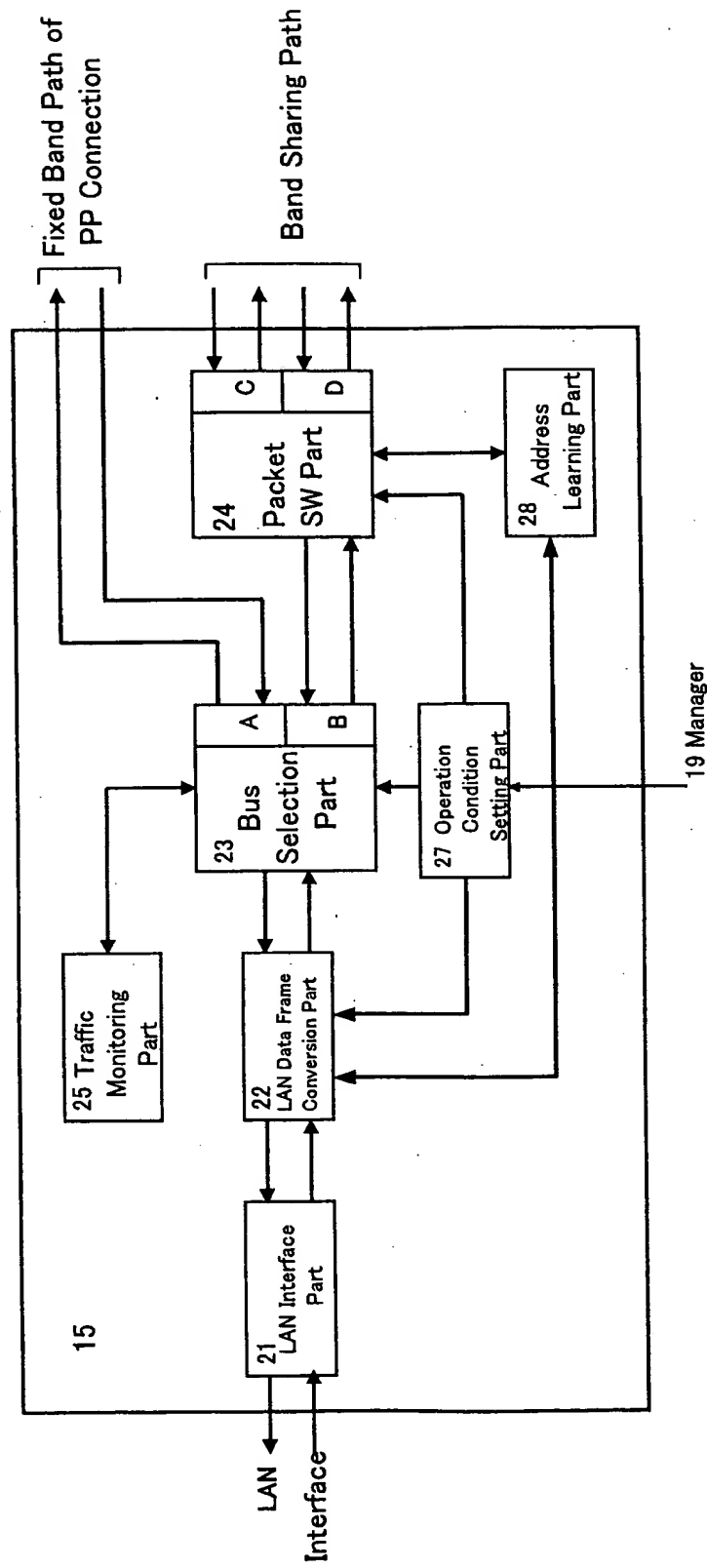


FIG. 10

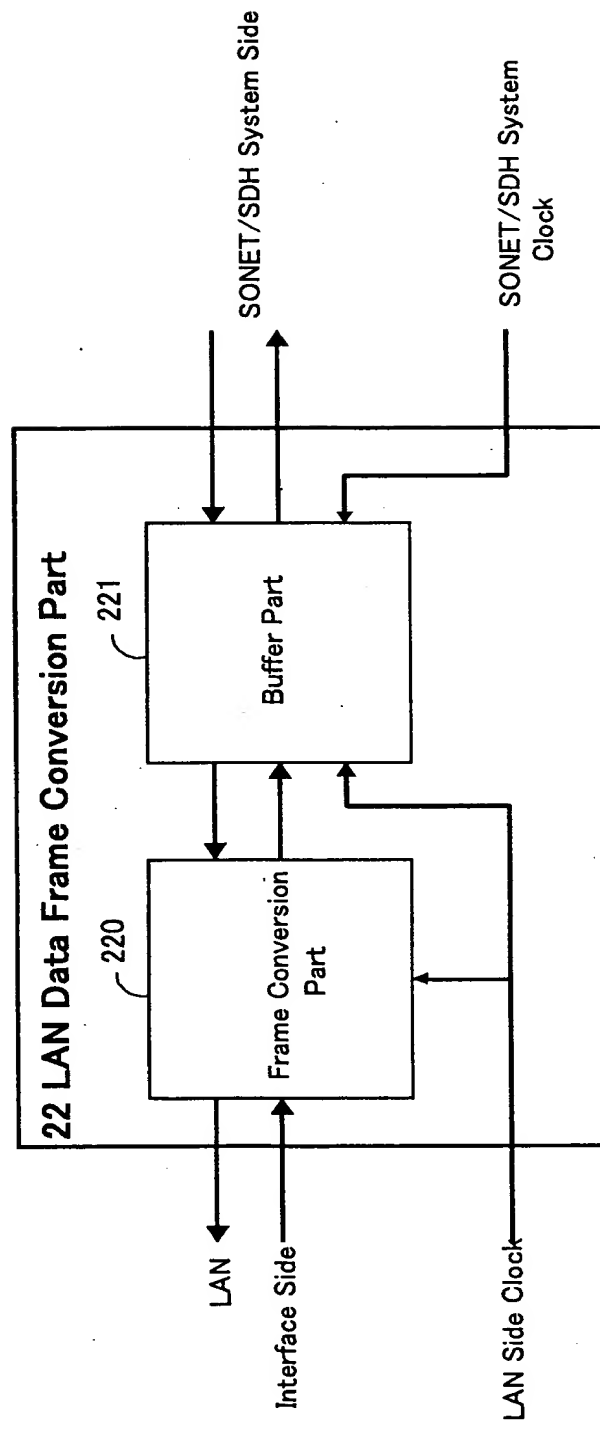


FIG. 11A

LAN Data

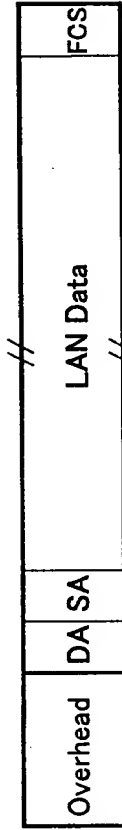
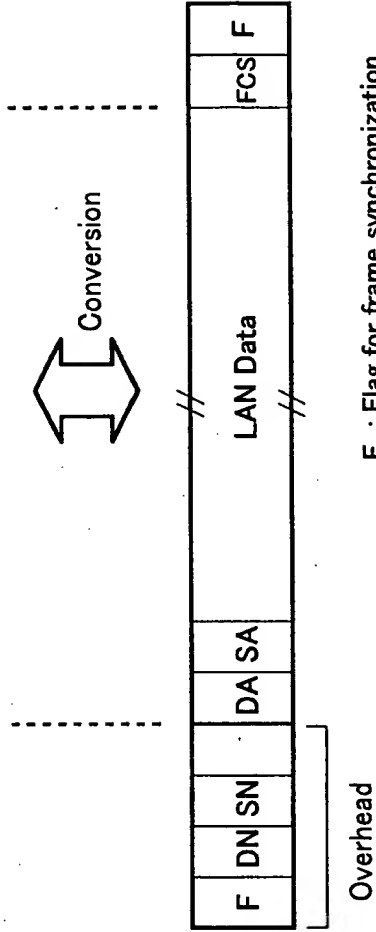


FIG. 11B

SONET/SDH Side
Packet



F : Flag for frame synchronization
FCS: Data for data error detection in frame

FIG. 12

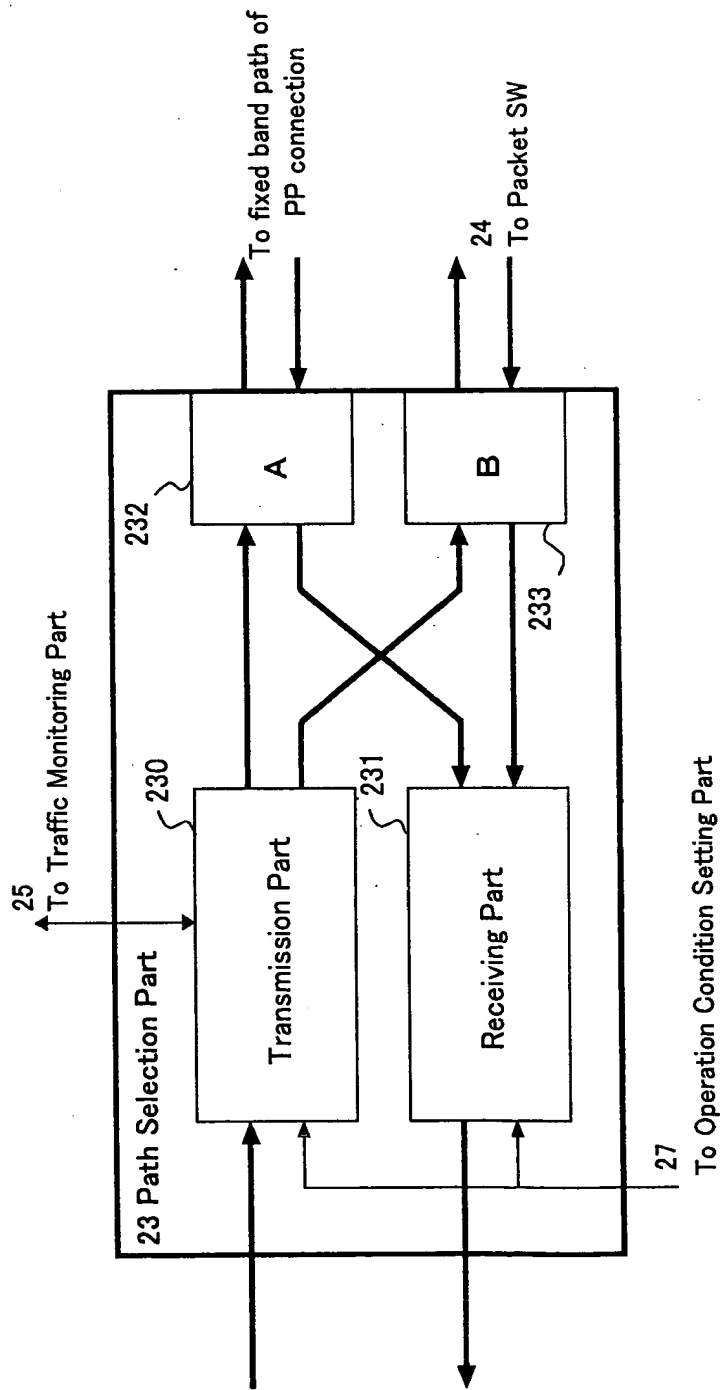


FIG. 13

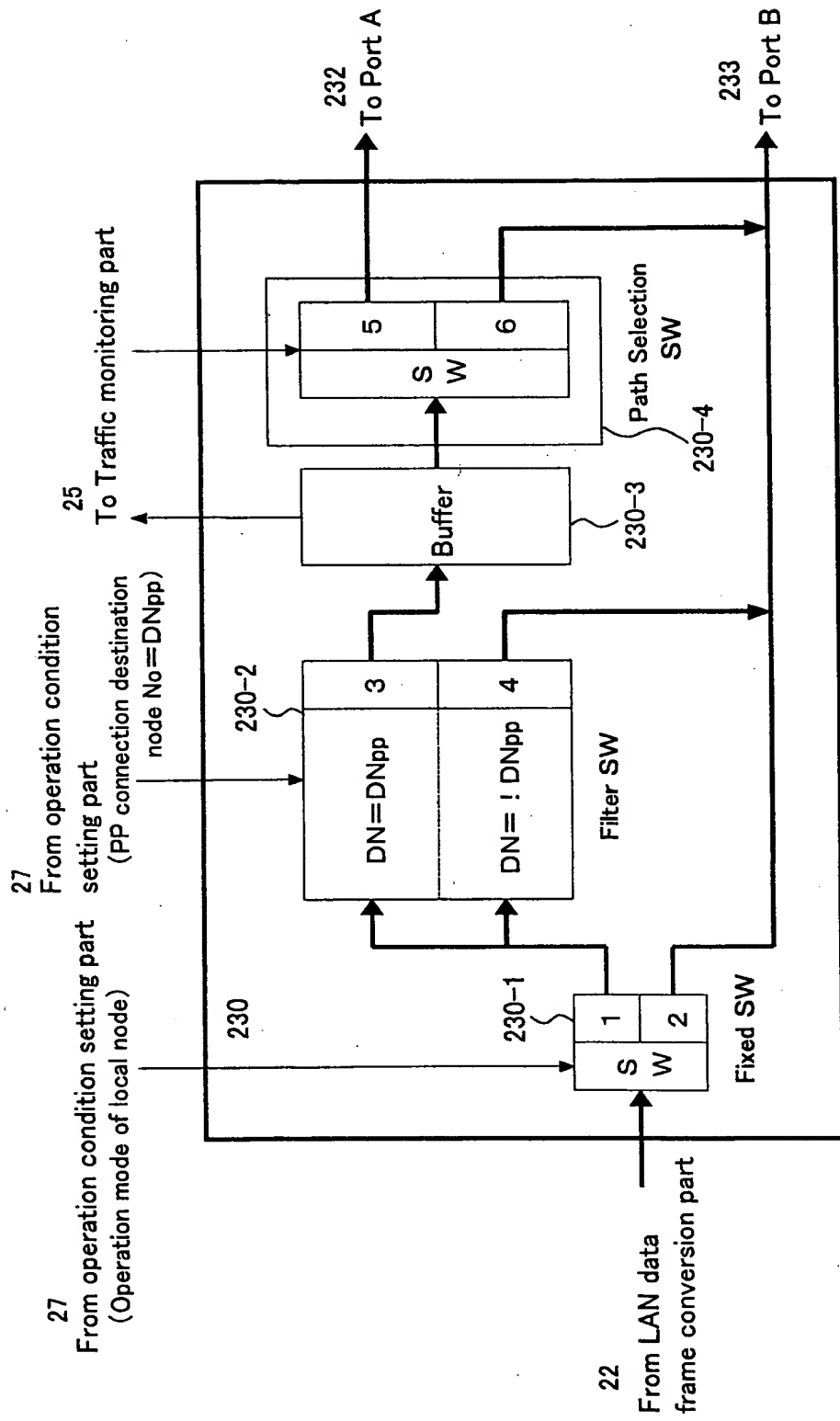


FIG. 14

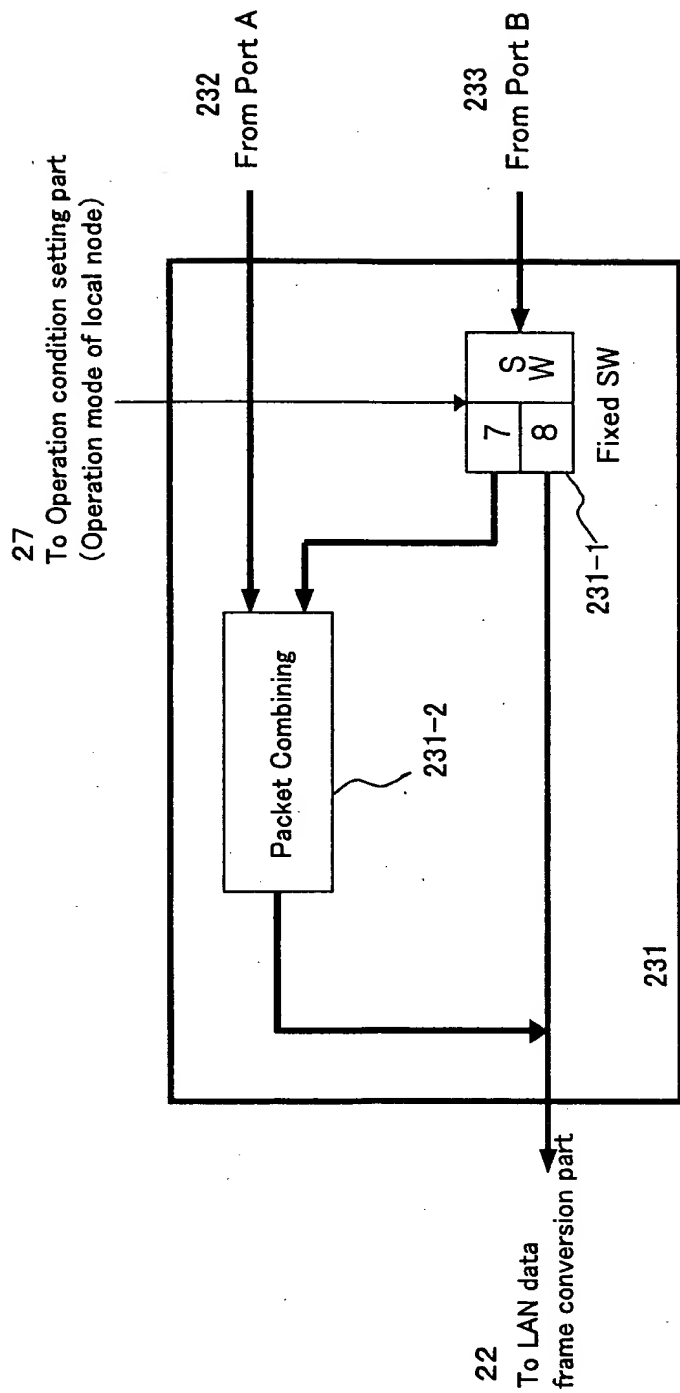
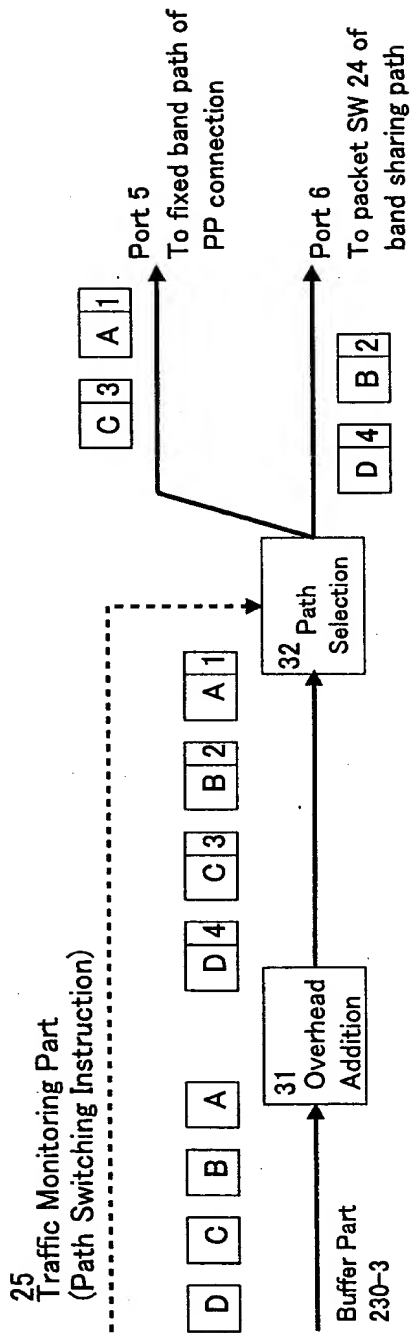


FIG. 15

FIG. 15



SONET/SDH Side Packet

FIG. 16

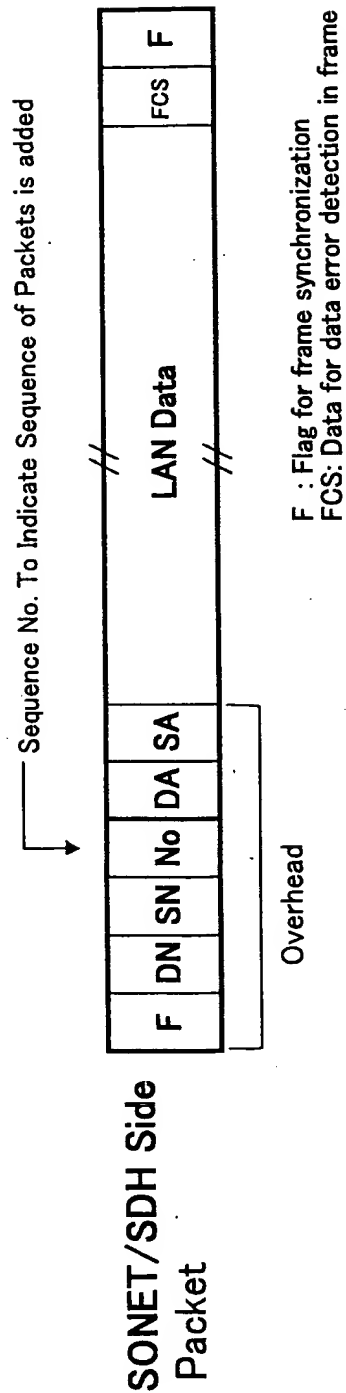


FIG. 17

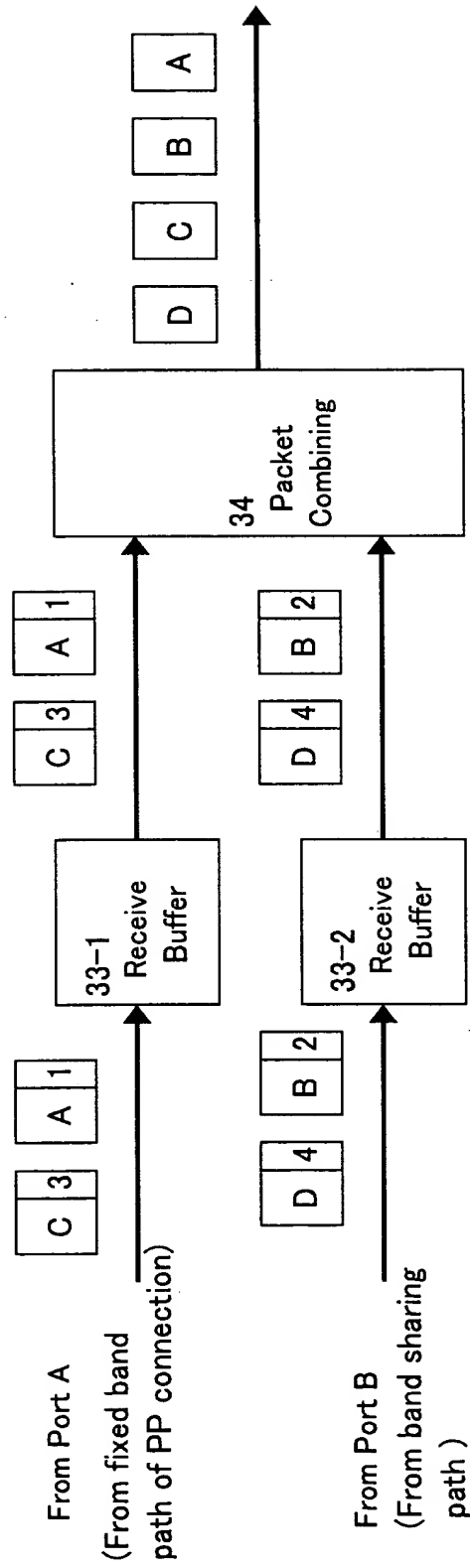


FIG. 18

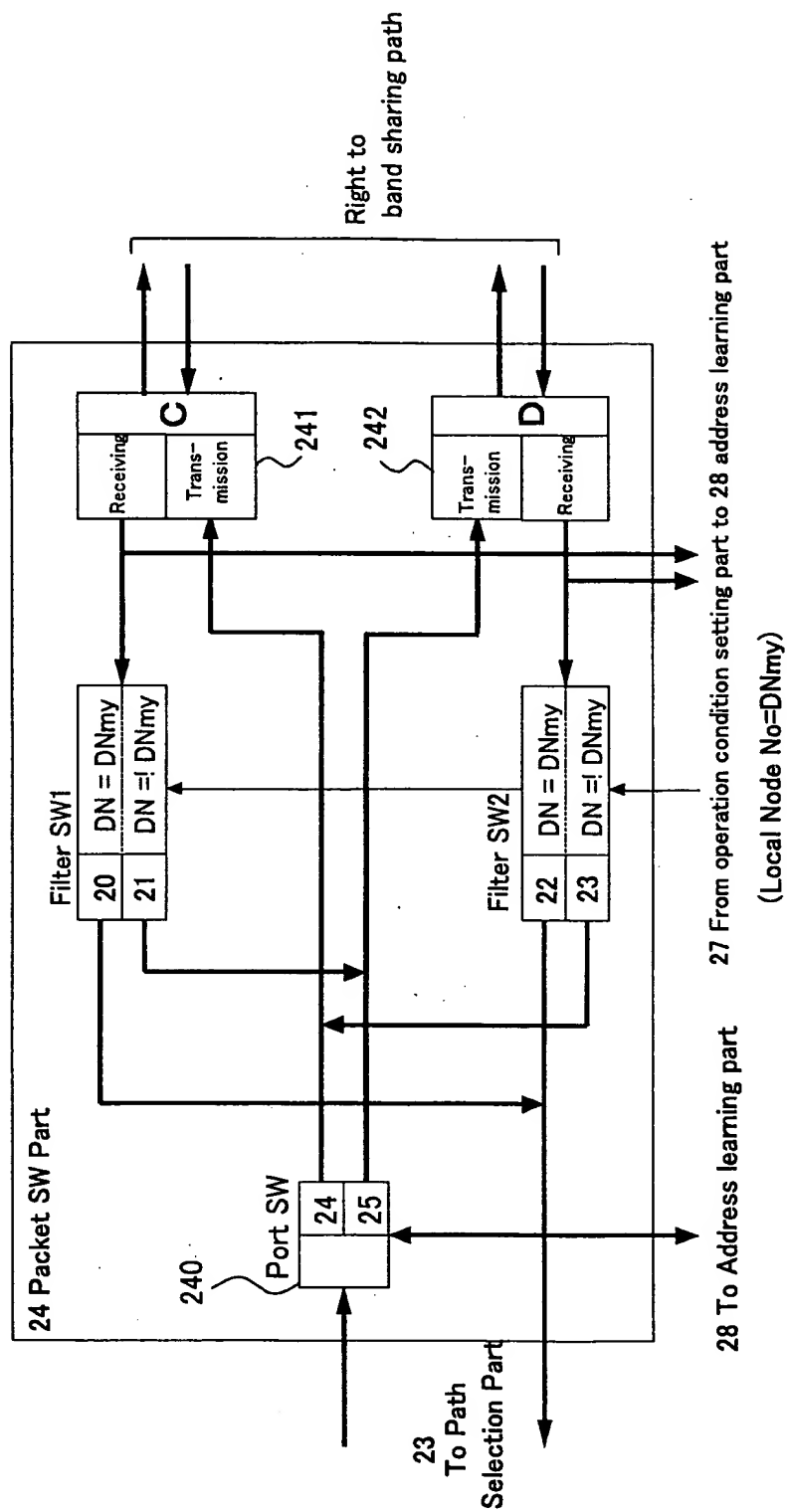


FIG. 19

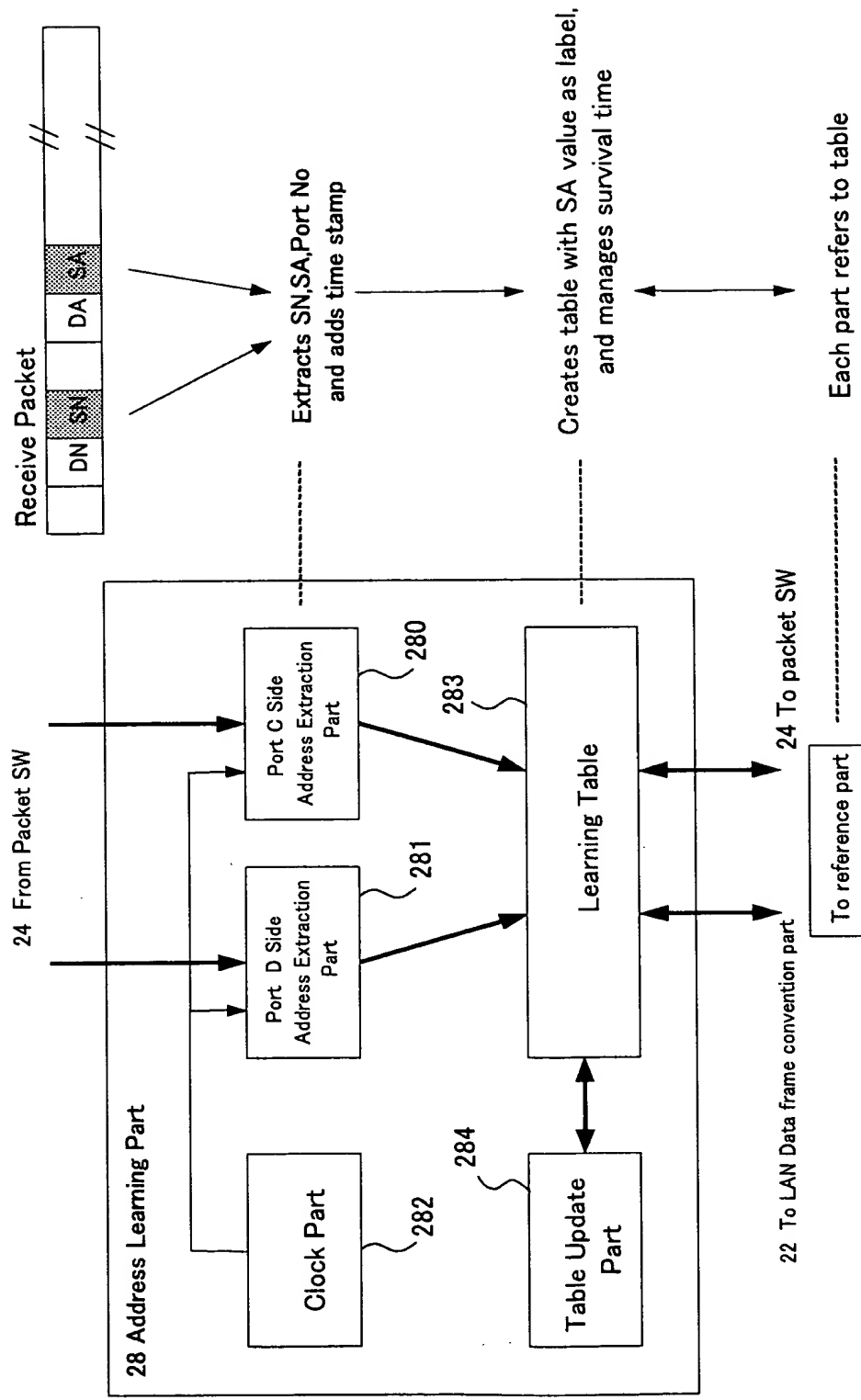


FIG. 20

Label (SA value from another node) (DA value from local node)	Data 1 (SN value from another node) (DN value from local node)	Data 2 (Node arrangement C or D)	Data 3 (Learning time)
Value 1	01	D	Time Stamp 1
Value 2	03	C	Time Stamp 2
Value 3	04	C	Time Stamp 3
.	.	.	.
.	.	.	.
.	.	.	.

Example of Learning Table at Node 2